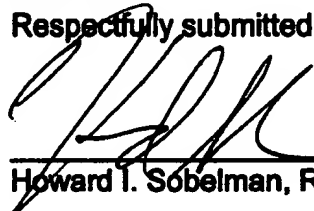


**REMARKS**

Applicant thanks Examiner for the phone call on April 29, 2003, suggesting that this 312 Amendment be filed. Applicant has reviewed the Notice of Allowance and has the foregoing amendments for clarification.

Thus, Applicant respectfully requests entry of the foregoing amendment. Attached hereto is a marked-up version of the changes made by the current amendment. The attached page is captioned "Version With Markings To Show Changes Made". Support for the various amendments may be found in the originally filed specification, claims, and figures. No new matter has been presented by this amendment. Applicant invites the Office to telephone the undersigned if he or she has any questions whatsoever regarding this Amendment or the present application in general.

Respectfully submitted,



Dated: May 1, 2003

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**"Version With Markings To Show Changes Made"**  
**In the Claims**

Please amend claims 52-54, 56 and 61 as follows:

52. (Currently Amended) The control and monitoring system of claim 49 48, wherein said second connector comprises a fifteen position D-sub connector and each of said plurality of first connectors comprises a fifteen position D-sub connector.

53. (Currently Amended) The control and monitoring system of claim 49 48, wherein said second connector comprises a fifteen position DB 15 connector and each of said plurality of first connectors comprises a DB 15 connector.

54. (Currently Amended) The control and monitoring system of claim 49 48, wherein the plurality of first connectors comprises at most sixteen connectors.

56. (Currently Amended) The system of claim 55 wherein said video driver means comprises:

(a) an on screen graphics display circuit, for generating text and graphics for an on screen menu; and

(b) an on screen graphics overlay circuit coupled to said on screen graphics display circuit and said video switch; and

(c) a plurality of first op-amp amplifying circuits coupled to said on screen graphics overlay circuit, one each for each of a plurality of red video signals from said on screen graphics overlay circuit; and

(d) a plurality of second op-amp amplifying circuits coupled to said on screen graphics overlay circuit, one each for each of a plurality of green video signals from said on screen graphics overlay circuit; and

(e) a plurality of third op-amp amplifying circuits coupled to said on screen graphics overlay circuit, one each for each of a plurality of blue video signals from said on screen graphics overlay circuit; and

(f) a first signal splitting circuit coupled to said video switch, for passing a plurality of vertical sync signals from said programmable logic means; and

(g) a second signal splitting circuit coupled to said video switch means, for passing a plurality of horizontal sync signals from said logic means,

whereby a plurality of video display devices may be sent video signals to drive said plurality of video display devices.

61. (Currently Amended) The system of claim 60 55 wherein said first programmable logic device comprises a Complex Programmable Logic Device; and said second programmable logic device comprises a Complex Programmable Logic Device.